

temp.wsp:1

- Drafts
- Pending
- Active
 - L1: (2) ("20050078514").PN.
 - L2: (684902) blocks
 - L3: (1) 1 and 2
 - L4: (1794097) read or reading or reads
 - L5: (41710) wordlines or (word adj lines)
 - L6: (3738549) nonvolatile or (non adj volatil
 - L7: (5830) 4 near8 5
 - L8: (500) 7 with 6
 - L9: (58) simultaneous or simultanuously
 - L10: (453359) simultaneous or simultenuously
 - L11: (32) 7 with 10
 - L12: (20) 11 and 6
 - L13: (198066) nonvolatile or (non adj volatil
 - L14: (14) 11 and 13
- Failed
 - 4 near8 6
 - 7 iwth 6
- Saved
- Favorites
- Tagged (0)
- UDC
- Queue
- Trash

Search List Browse Queue Clear

DBs US-PGPUB; USPAT; EPO; JPO; DI Plurals

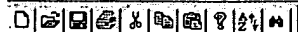
Default operator: OR Highlight all hit terms initially

Search Report Printout

7/22/05 AM

BRS form IS&R form Image Text HTML

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	E
1	IS&R	L1	2	("20050078514").PN.	US-PG PUB;	2005/07/20 11:38			
2	BRS	L2	684902	blocks	US-PG PUB;	2005/07/20 11:38			
3	BRS	L3	1	1 and 2	US-PG PUB;	2005/07/20 11:41			
4	BRS	L4	1794097	read or reading or reads	US-PG PUB;	2005/07/20 11:41			
5	BRS	L5	41710	wordlines or (word adj	US-PG	2005/07/20			



temp.wsp:1

- ☐ Drafts
- ☐ Pending
- ☒ Active
 - L1: (2) ("20030161197").PN.
 - L2: (75299) dummy
 - L3: (3733190) reference
 - L4: (1376535) cell or cells
 - L5: (18642) (2 or 3) adj2 4
 - L6: (41756) wordlines or (word adj lines)
 - L8: (3487) 365/230.03.ccls.
 - L11: (1206) 5 with 6
 - L12: (47) 8 and 11
 - L13: (2) ("6104653").PN.
 - L14: (1) 6 and 13
 - L15: (459) complement adj2 (bitline or (bit adj line))
 - L16: (4706) sharS3 near5 sense
 - L17: (1982) complementary adj2 (bitline or (bit adj line))
 - L18: (10) (15 or 17) with 16
 - L19: (50) (15 or 17) same 16
 - L20: (8) 19 and 8
- ☐ Failed
- ☐ Saved
- ☐ Favorites
- ☐ Tagged (1)
- ☐ UDC
- ☐ Queue
- ☐ Trash

Search List Browse Queue Clear

DBs US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB

☐ Purds

Default operator: OR

☒ Highlight all hit terms initially

19 and 8

Search Report printout
7/22/05 HNM

BRS form IS&R form Image Text HTML

	U		Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C	Inventor
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6909646 B2	20050621	21	Semiconductor memory device	365/200	365/189.07;		Hasegawa; Masa
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6831870 B2	20041214	14	Semiconductor memory	365/200	365/230.03		Koga; Toru
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6603688 B2	20030805	22	Semiconductor memory device	365/200	365/201;		Hasegawa; Masa
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6157560 A	20001205	18	Memory array datapath archit	365/63	365/230.02;		Zheng; Hua
5	<input type="checkbox"/>	<input type="checkbox"/>	US 6115319 A	20000905	43	Dynamic RAM having word line	365/233	365/149;		Kinoshita; Yos
6	<input type="checkbox"/>	<input type="checkbox"/>	US 6094396 A	20000725	11	Memory array architecture fo	365/230.03	365/189.04		Zheng; Hua
7	<input type="checkbox"/>	<input type="checkbox"/>	US 5970003 A	19991019	30	Semiconductor memory device	365/200	365/225.7;		Miyatake; Shin
8	<input type="checkbox"/>	<input type="checkbox"/>	US 5761149 A	19980602	23	Dynamic RAM	365/230.03	257/E27.081;		Suzuki; Yukihi